

FIG. 1A

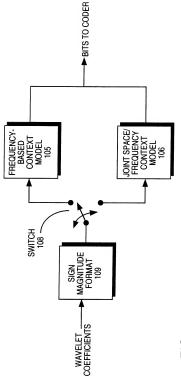


FIG. 1B

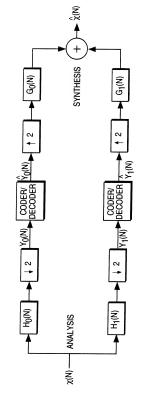


FIG. 2A

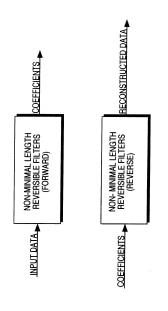


FIG. 2B

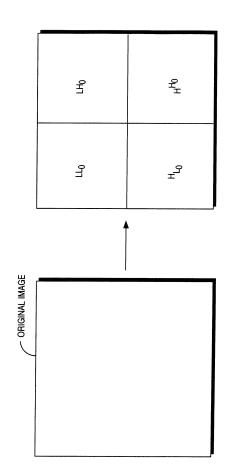


FIG 3A

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FIG. 3B

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FIG. 3C

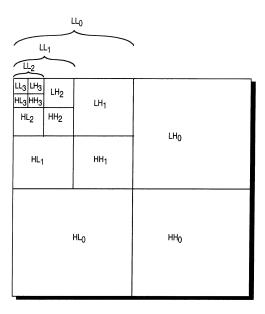


FIG. 3D

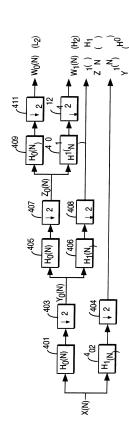
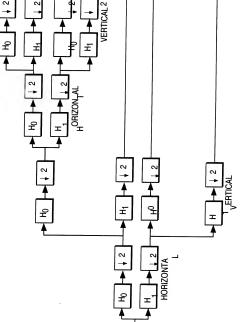
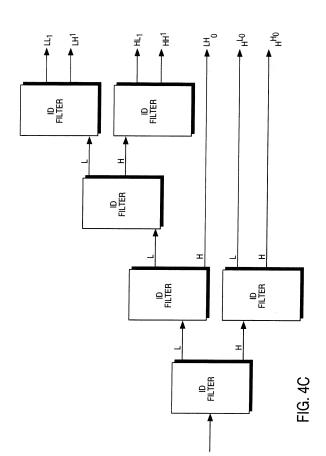


FIG. 4A



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F<sub>lG</sub>. 4B



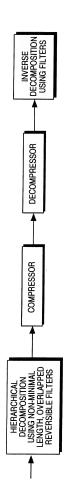


FIG. 4D

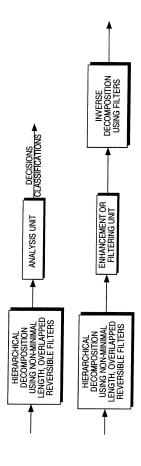


FIG. 4E

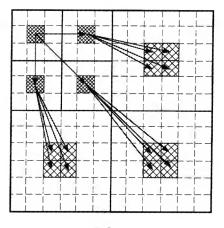
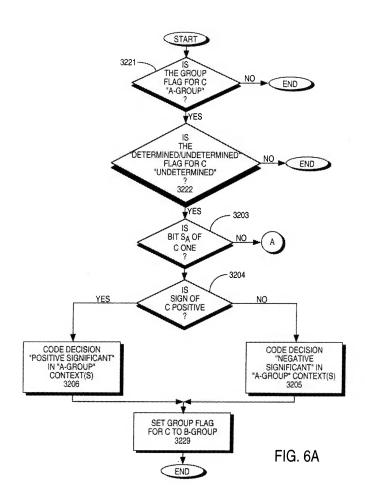
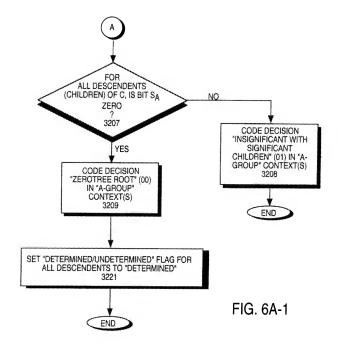
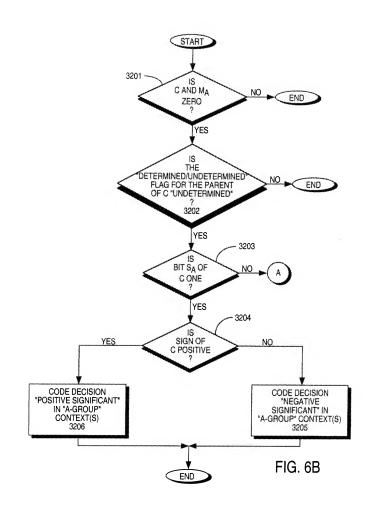
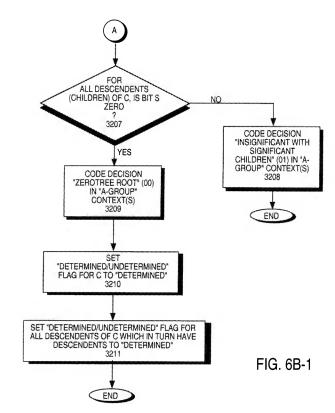


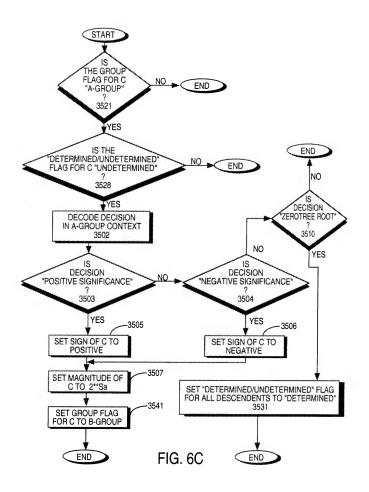
FIG. 5

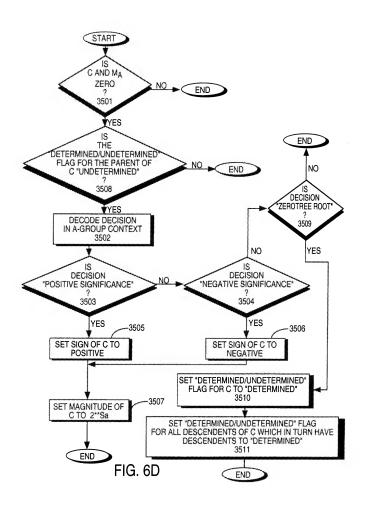


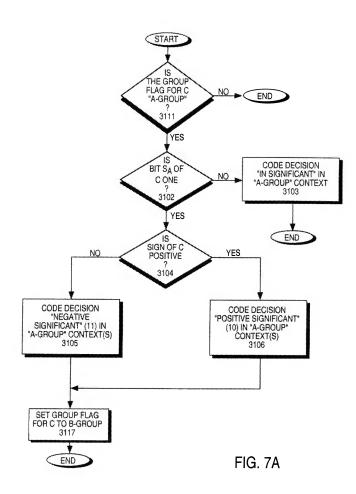












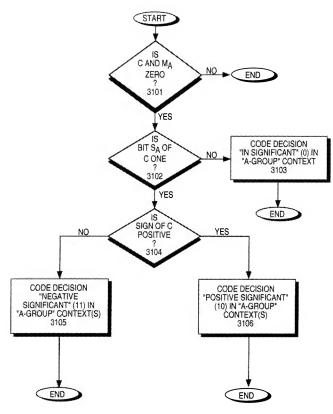
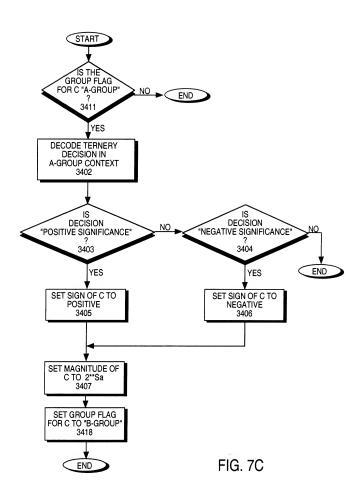
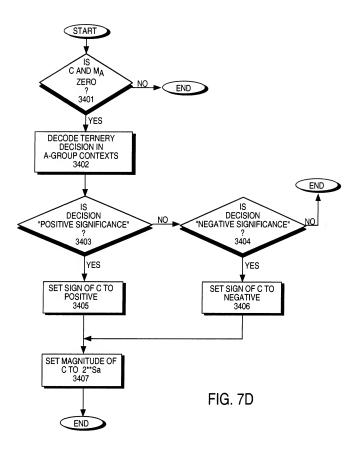
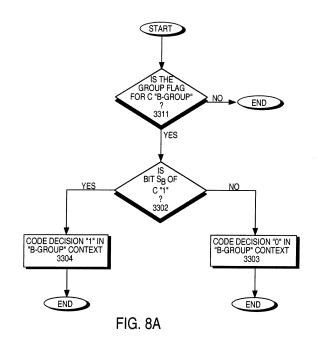


FIG. 7B







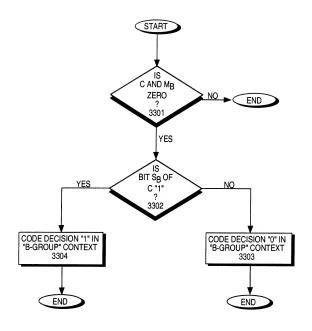
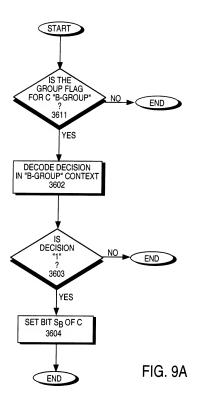
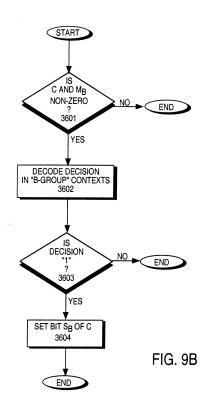


FIG. 8B





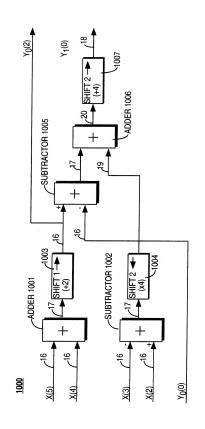
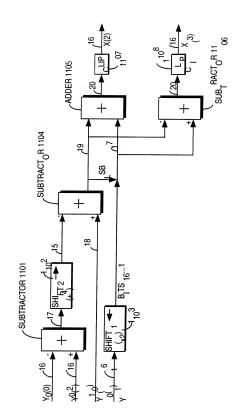
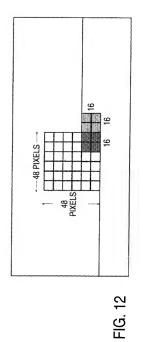


FIG. 10



\_.c



FILTER 1303 DATA MEMORY 1302 ADDRESS , FILTER CONTROL 1301 FIG. 13

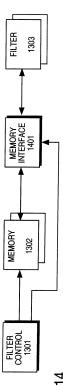
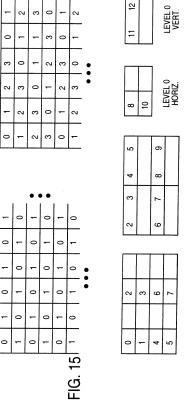


FIG. 14



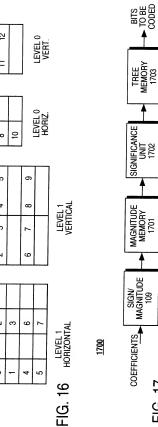
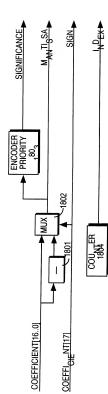


FIG. 17



FlG. 18

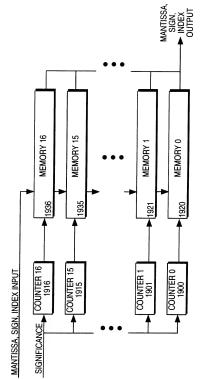


FIG. 19

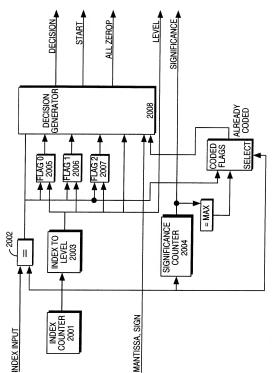


FIG. 20

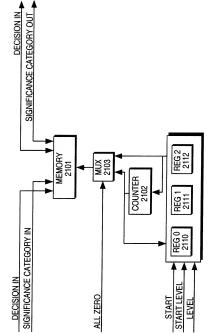


FIG. 21



FIG. 22

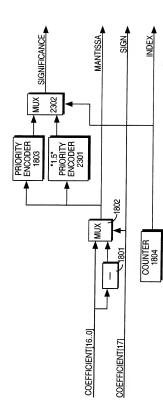


FIG. 23

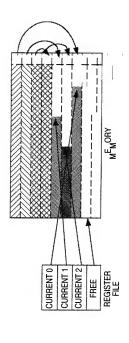
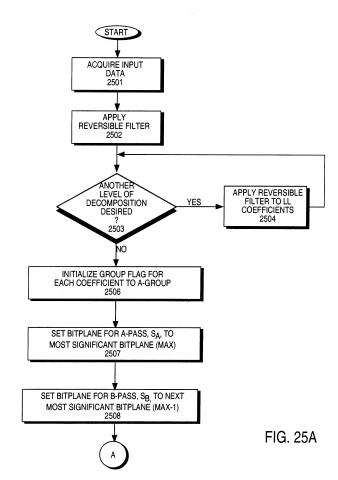
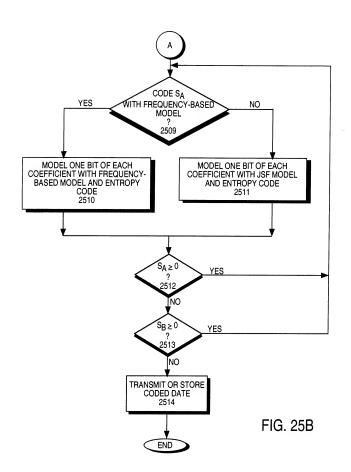
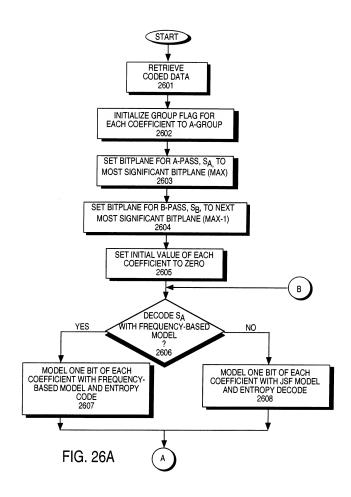
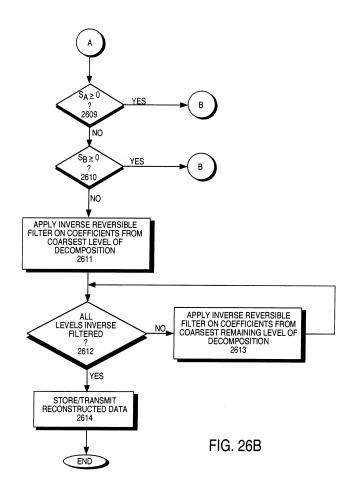


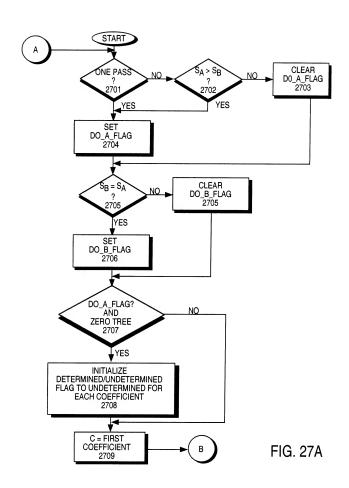
FIG. 24

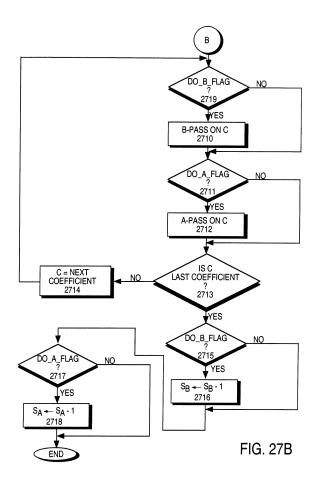


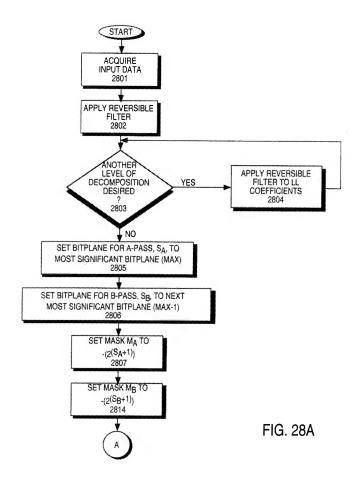


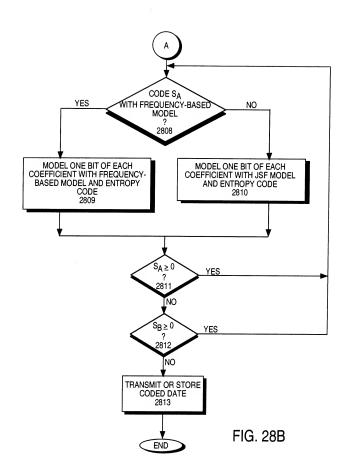


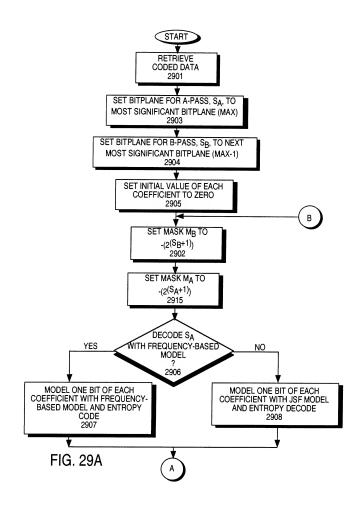


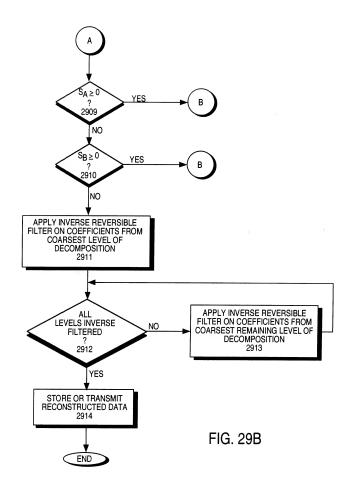


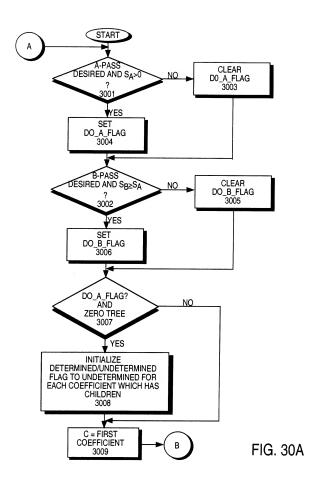


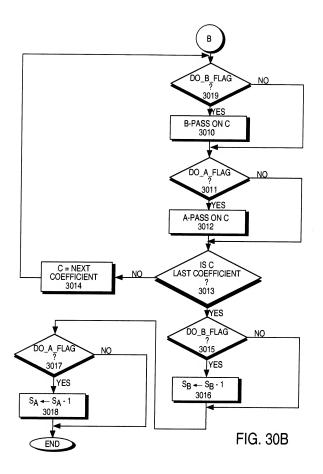












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FIG. 31